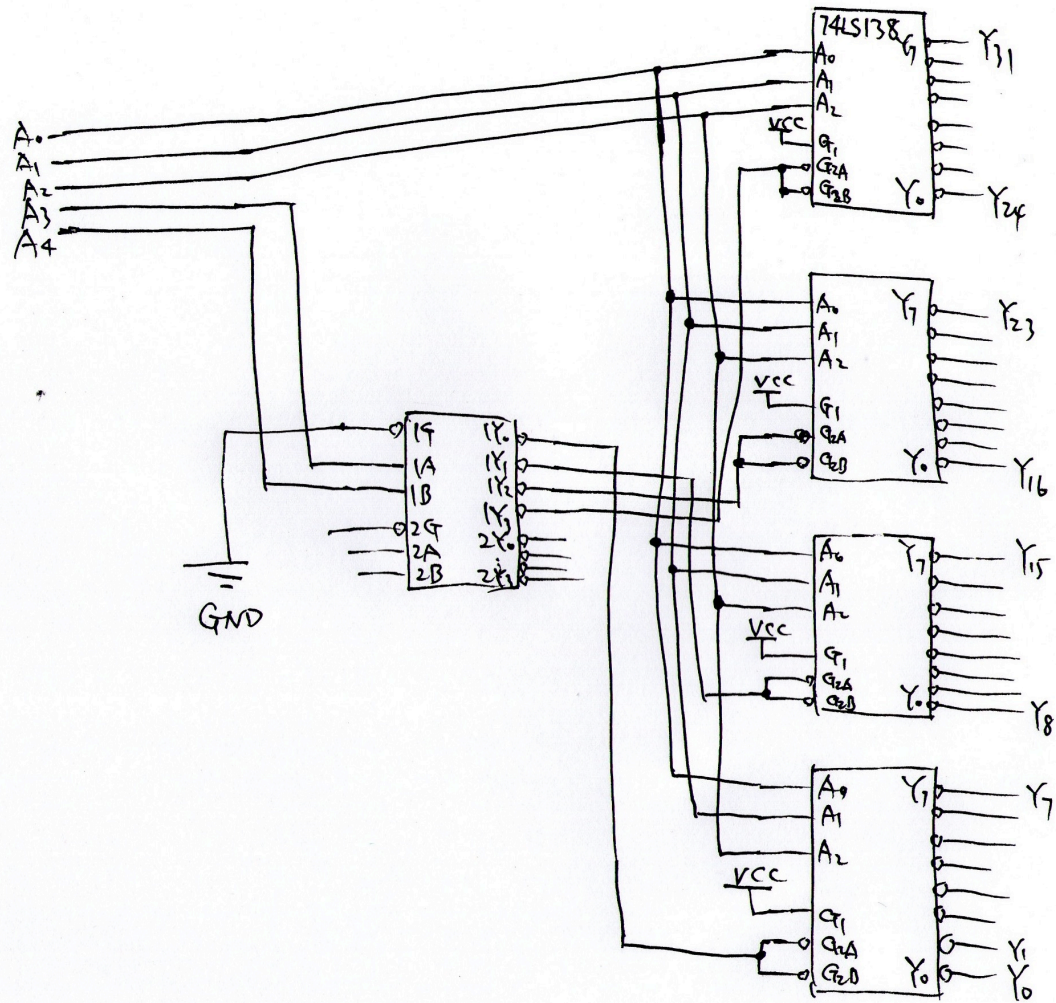


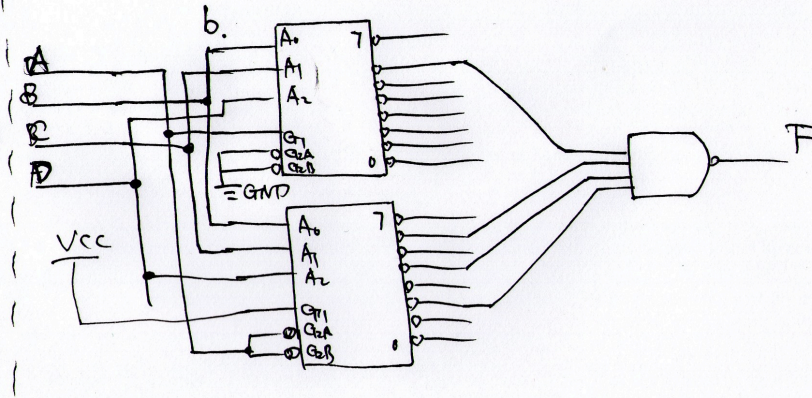
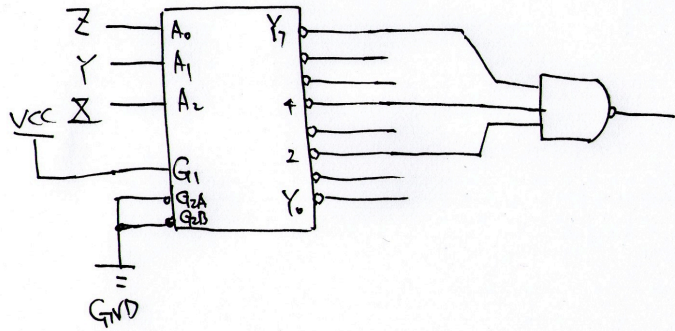
### Solutions to Problem Set # 7

1. Design a 5-line to 32-line decoder with 74LS138 and 74LS139.
2. Implement the following logic function with 74LS138 and NAND gate
  - a.  $F = \sum_{x,y,z}(2,4,7),$
  - b.  $F = \sum_{a,b,c,d}(2,4,6,14)$
3. Repeat problem 2 with 74X151

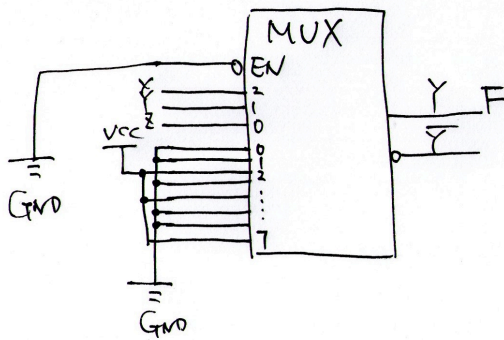
1.  
Solution :



2. a.



3. a.



b.

